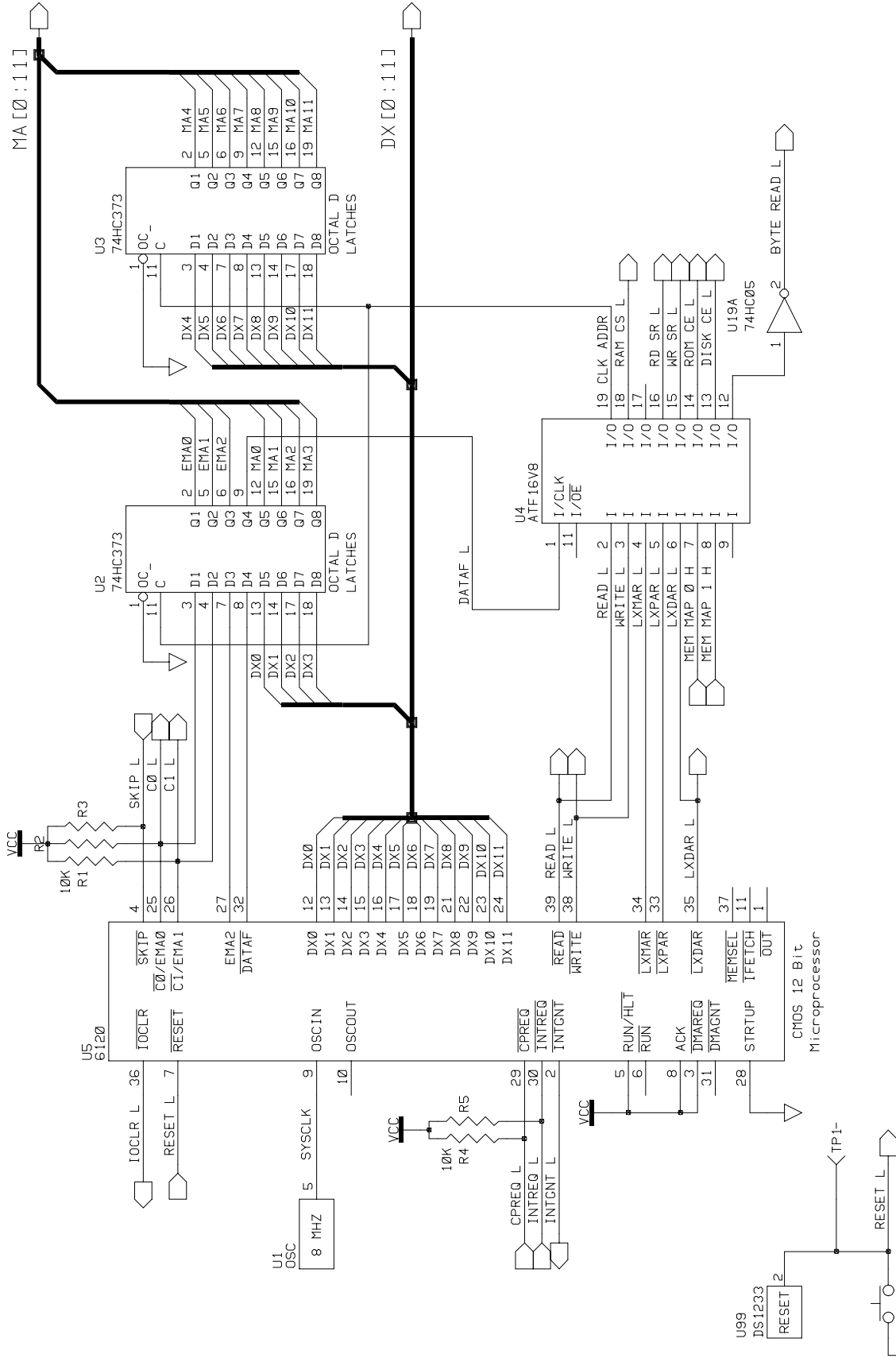


8 7 6 5 4 3 2 1



SBC6120-2  
CPU

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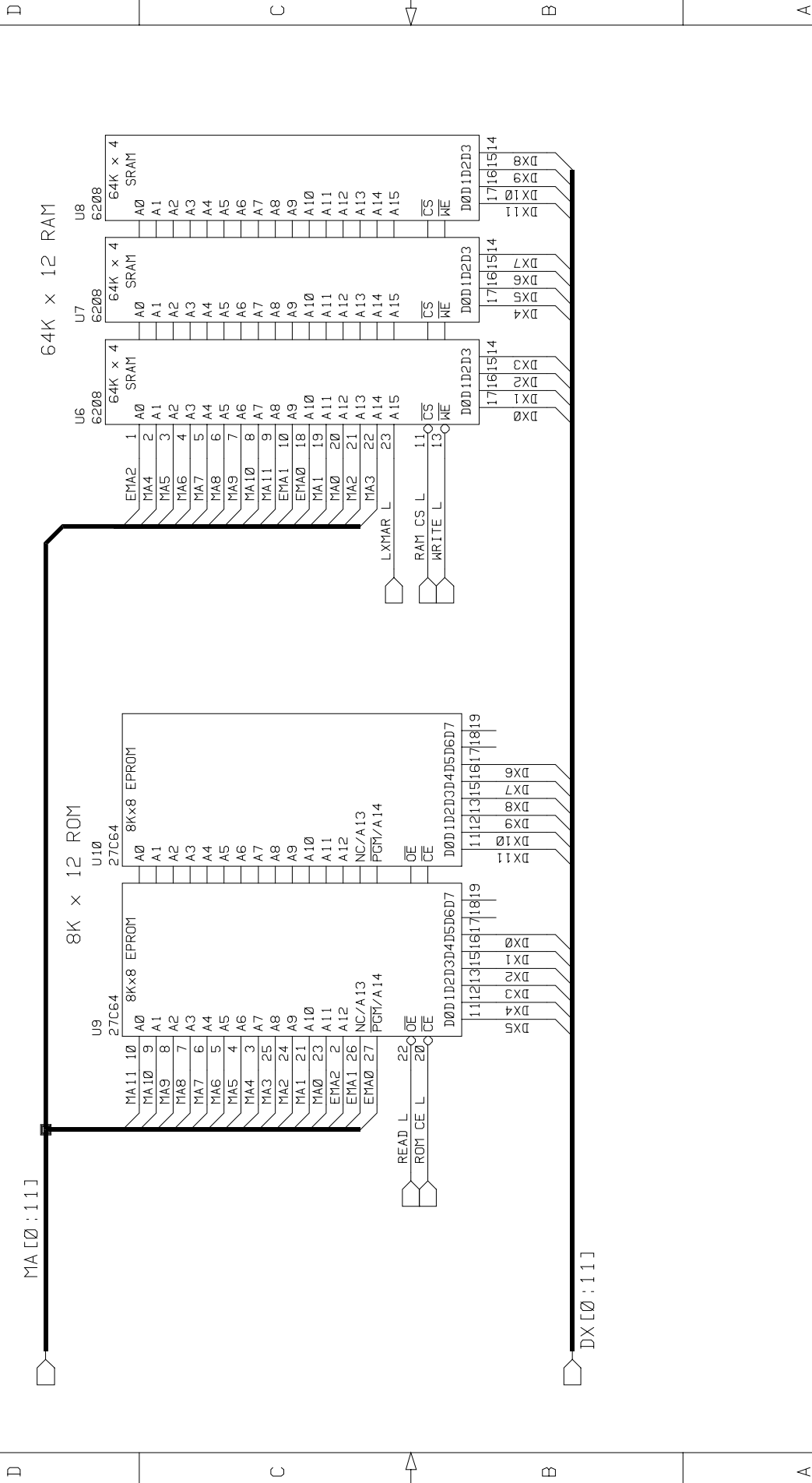
SIZE C  
SHEET 1 OF 5

1 2 3 4 5 6 7 8

D C B A

D C B A

8 7 6 5 4 3 2 1



SBC6120-2  
MEMORY

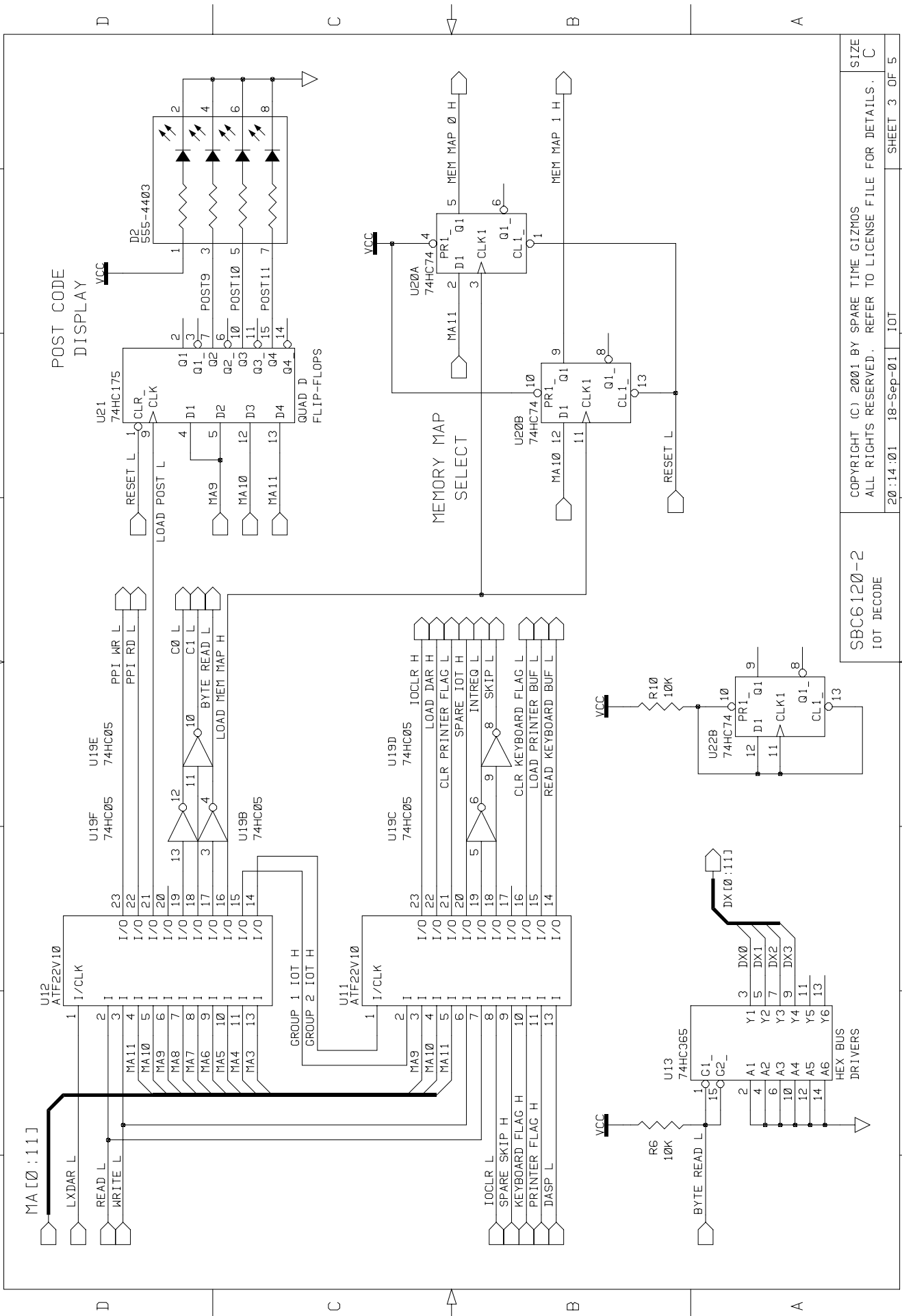
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SIZE C  
SHEET 2 OF 5

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

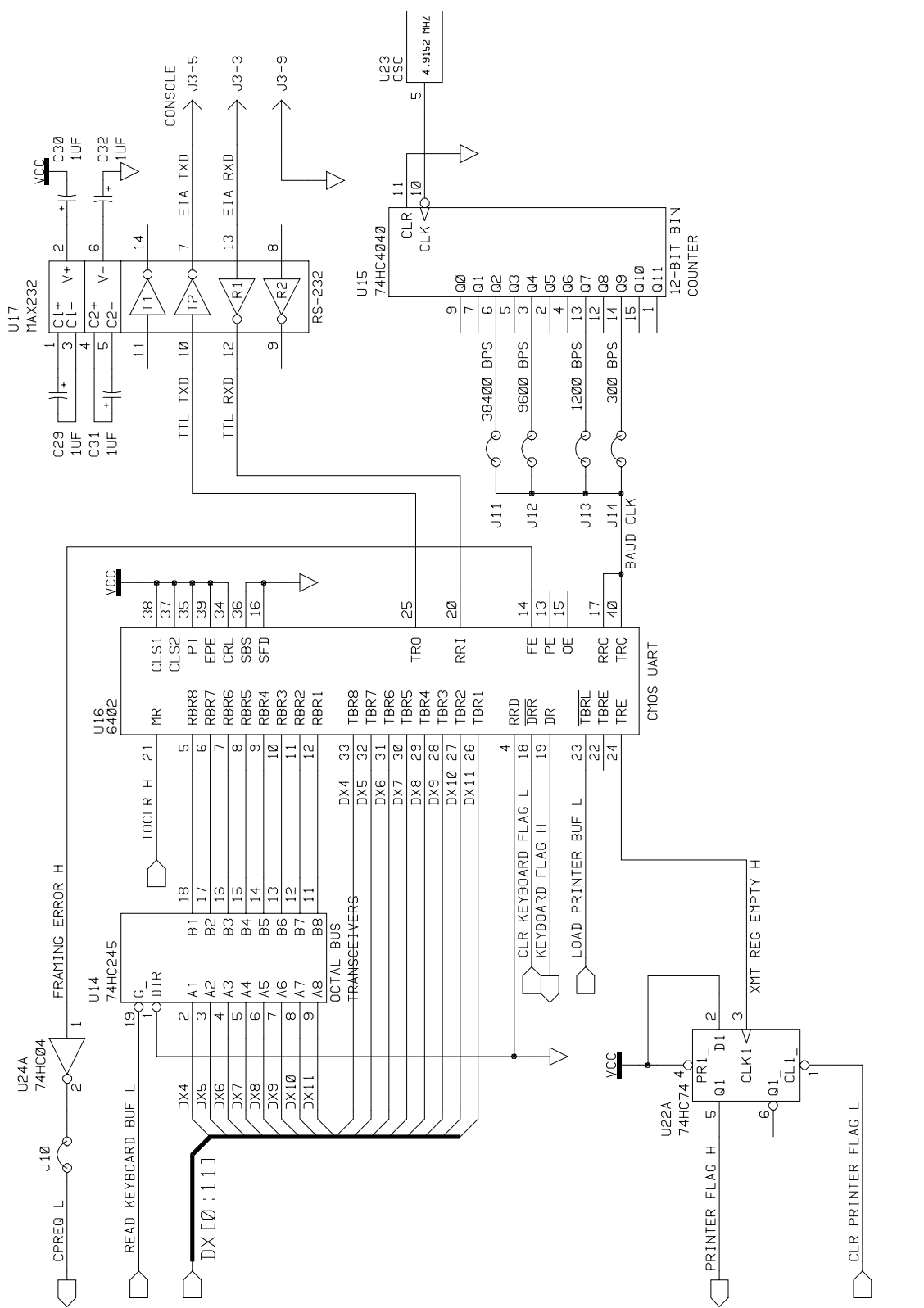


SBC6120-2  
IOT DECODE

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20:14:01 18-Sep-01 IOT

8 7 6 5 4 3 2 1



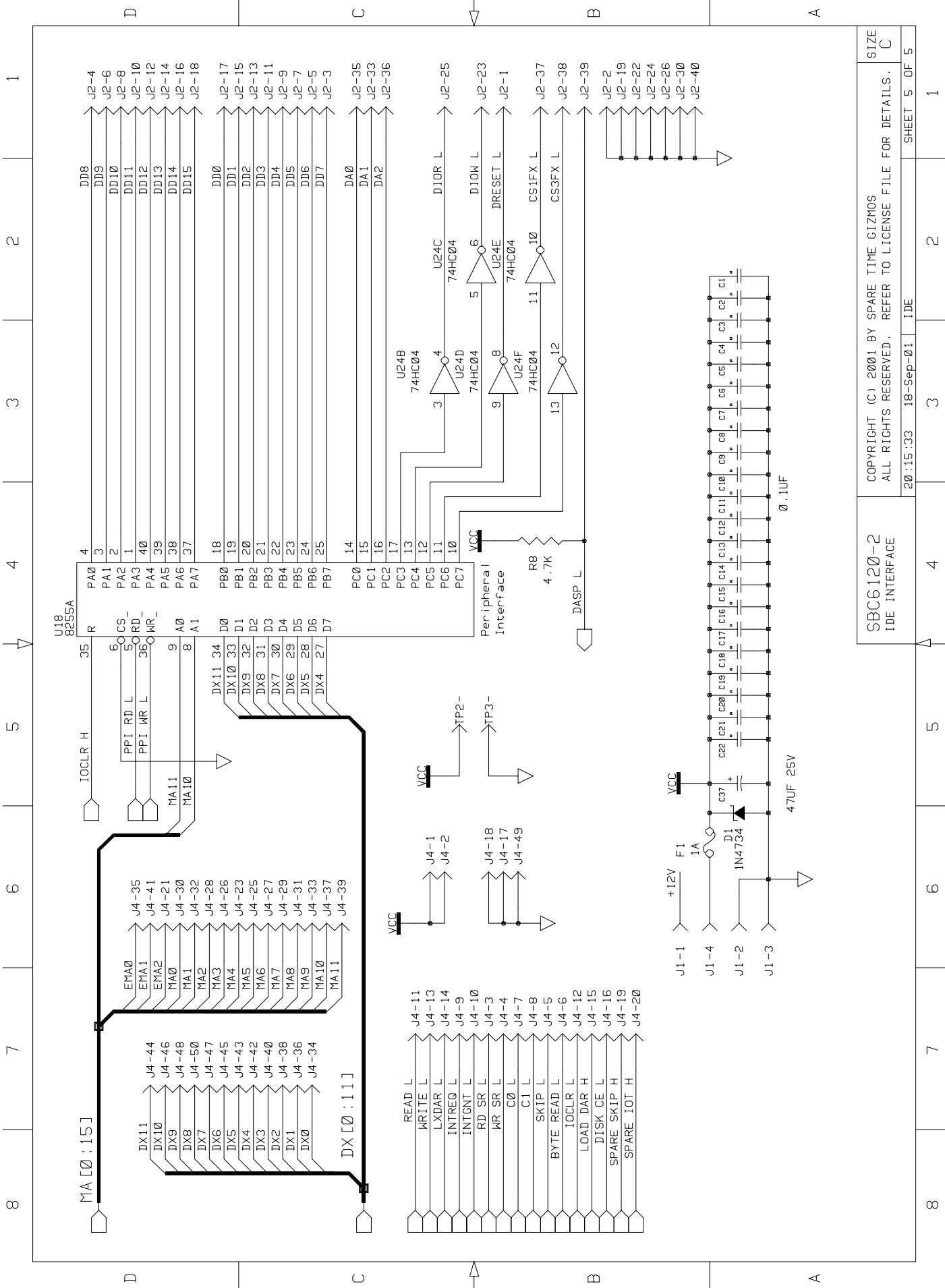
D C B A

SBC6120-2  
KL8 CONSOLE  
INTERFACE

20:14:37 18-Sep-01 CONSOLE

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SIZE C SHEET 4 OF 5



SBC6120-2  
IDE INTERFACE

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20:15:33 18-Sep-01 IDE SHEET 5 OF 5